

Amendments to the Claims:

Claim 1. (Original): An arrangement for providing an improved common mode rejection ratio in a load driver circuit having an input voltage and a ground voltage, said arrangement comprising:

a voltage regulator adapted to regulate said ground voltage in response to variations in said input voltage and coupled to provide a regulated ground voltage;

amplifier means including sense inputs coupled to receive voltage signals from a sense resistor of said load driver circuit, for providing an amplified output voltage;

wherein said amplifier means is adapted to be powered by power terminals coupled to said input voltage and said regulated ground voltage respectively, such that the common mode rejection ratio of the load driver circuit is reduced.

Claim 2. (Original): The arrangement of claim 1 wherein said amplifier means includes first and second differential amplifiers.

Claim 3. (Original): The arrangement of claim 2 wherein said first differential amplifier has power terminals coupled to said input voltage and said regulated ground voltage respectively and said second differential amplifier has power terminals coupled to said input voltage and said ground voltage respectively.

Claim 4. (Original): The arrangement of claim 3 wherein said first and second differential amplifiers each have gain values adapted to produce a circuit gain value of substantially unity.

Claim 5. (Original): The arrangement of claim 4 wherein said second differential amplifier has a non-inverting input coupled to an output of said first differential amplifier and an inverting input coupled to said regulated ground voltage.

Claim 6. (Original): A load driver circuit comprising:
an arrangement for providing an improved common mode rejection ratio in said load driver circuit having an input voltage and a ground voltage, said arrangement comprising:
a voltage regulator adapted to regulate said ground voltage in response to variations in said input voltage and coupled to provide a regulated ground voltage;
amplifier means including sense inputs coupled to receive voltage signals from a sense resistor of said load driver circuit, for providing an amplified output voltage;
wherein said amplifier means is adapted to be powered by power terminals coupled to said input voltage and said regulated ground voltage respectively, such that the common mode rejection ratio of the load driver circuit is reduced.

Claim 7. (Currently amended): The load driver circuit of claim 6 wherein said load driver circuit is a high side driver circuit comprising a high side current sense resistor.

Claim 8. (Original): The circuit of claim 7 wherein said amplifier means includes first and second differential amplifiers.

Claim 9. (Original): The circuit of claim 8 wherein said first differential amplifier has power terminals coupled to said input voltage and said regulated ground voltage respectively and said second differential amplifier has power terminals coupled to said input voltage and said ground voltage respectively.

Claim 10. (Original): The circuit of claim 9 wherein said first and second differential amplifiers each have gain values adapted to produce a circuit gain value of substantially unity.

Claim 11. (Original): The circuit of claim 10 wherein said second differential amplifier has a non-inverting input coupled to an output of said first differential amplifier and an inverting input coupled to said regulated ground voltage.

Claim 12. (Original): A method for providing an improved common mode rejection ratio in a load driver circuit having an input voltage and a ground voltage, said method comprising the steps of :

regulating said ground voltage in response to variations in said input voltage in order to provide a regulated ground voltage;

amplifying voltage signals received from a sense resistor of said load driver circuit using amplifier means, for providing an amplified output voltage;

wherein said amplifier means is adapted to be powered by power terminals coupled to said input voltage and said regulated ground voltage respectively, such that said common mode rejection ratio of said load driver circuit is reduced.

Claim 13. (Original): The method of claim 12 wherein said amplifier means includes first and second differential amplifiers.

Claim 14. (Original): The method of claim 13 wherein said first differential amplifier has power terminals coupled to said input voltage and said regulated ground voltage respectively and said second differential amplifier has power terminals coupled to said input voltage and said ground voltage respectively.

Claim 15. (Original): The method of claim 14 wherein said first and second differential amplifiers each have gain values adapted to produce a circuit gain value of substantially unity.

Claim 16. (Original): The method of claim 15 wherein said second differential amplifier has a non-inverting input coupled to an output of said first differential amplifier and an inverting input coupled to said regulated ground voltage.